

Electronic Acknowledgement Receipt

EFS ID:	5802649
Application Number:	10518974
International Application Number:	
Confirmation Number:	2918
Title of Invention:	Microprocessor instruction execution method for exploiting parallelism by time ordering operations in a single thread at compile time
First Named Inventor/Applicant Name:	Richard Michael Taylor
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Filer Authorized By:	Mark D. Simpson
Attorney Docket Number:	356884.00002-US
Receipt Date:	30-JUL-2009
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
1		int6D.PDF	110591 d81c7331cb66075ef8f50485c6e6b6ca7a7d2 cc1b	yes	9
Multipart Description/PDF files in .zip description					
Document Description	Start	End			
Response after Ex Parte Quayle Action	1	1			
Claims	2	7			
Applicant Arguments/Remarks Made in an Amendment	8	9			

Warnings:**Information:**

2	Fee Worksheet (PTO-875)	fee-info.pdf	30327 b6f97c474a3eb63d4e03d814289f164c82 d6666	no	2
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Warnings:**Information:****Total Files Size (in bytes):** 140918

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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